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Q2 2021 Earnings Call Aug 04, 2021, 5:00 p.m. ET Good afternoon, and welcome to SiTime's second-quarter 2021 financial results conference call. [Operator instructions] As a reminder, this conference ...

SiTime Corporation (SITM) Q2 2021 Earnings Call Transcript

AI systems can improve healthcare, increase recovery chances for patients and assist physicians in their diagnoses. The challenge is that artificial ...

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Next Generation Computing: Energy-efficient AI chips for atrial fibrillation detection

Because each logic block performs a specific ... complex programmable logic devices. Internal frequency is the speed at which CPLDs can perform operations or transfer data internally. The propagation ...

Complex Programmable Logic Devices (CPLD) Information

While the 5G NR architecture includes new spectrum and massive MIMO (mMIMO) antennas, corresponding access networks architecture must also evolve to implement the services defined ... latency, and ...

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Accelerating 5G Baseband With
Adaptive SoCs

Dubbed Modipwn, the vulnerability affects a wide variety of Modicon programmable ... building services, automation, energy utilities, HVAC and other industrial applications.

Vulnerability in Schneider Electric PLCs allows for undetectable remote takeover

In particular, demand for our programmable ... market over time. Our increased strategic focus on the non-China industrial and aerospace markets where we are delivering innovative solutions ...

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Call Transcript
Programmable Timing
Solutions
today announced the selection of Napatech's programmable Smart Network Interface Cards (SmartNICs) into the Cyan Storage and Playback Solution. Applications such as real-time spectrum monitoring ...

Per Vices Corporation Selects Napatech's Programmable SmartNICs to Enable High-Performance Spectrum Monitoring and Recording
World's First Programmable NFT ...
digital art to evolve over time, react to its owners, or follow any public data point. We sell different types of products and services to both investment ...

NETGEAR Partners With Async Art To

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Add Dynamic, Programmable NFTs To The Meural Library

New directions for programmable ... of diseases and applications is likely to expand the scope of genome editing in the near future. It includes understanding the role of specific genes and ...

Genome Editing Market is Presently Witnessing Significant Growth due to Increase in R&D Expenditure which produces rugged embedded computing solutions for ... modified micro-kernels with specific features to provide the real-time determinism needed for the application. Lynx's Ferguson says ...

New frontiers in real-time software

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As a result, GOWIN's ISP IP core portfolio can be combined with video interface, scalar, and memory controller IP's to create a complete programmable SoC solution tailored specifically to the ...

GOWIN Semiconductor Announces their ISP (Image Signal Processor) IP Core and Solution

A programmable blockchain is a place where developers can build decentralized applications ... products and services that we think might interest you. You can unsubscribe at any time.

Should You (or Anyone) Buy Polkadot?

4D printing is a groundbreaking

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method of manufacturing customized digital materials using a programmable ... application segment is classified into medical models, surgical guides, and patient ...

With 23.3% CAGR, Global 4D Printing in Healthcare Market Size & Share Will Reach USD 48.95 Billion by 2026:

Facts & Factors

Jul 13, 2021 (Heraldkeepers) -- The global Field Programmable Gate Array (FPGA ... language (HDL), similar to that used for an application-specific integrated circuit (ASIC) (circuit diagrams ...

Field Programmable Gate Array (FPGA) Market Research Report with Size, Share, Value, CAGR, Outlook,

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Analysis, Latest Updates, Data, and
News 2021

New Stack Provides Low Power,
Secure, and Reliable Solutions ... and
software-programmable reference
designs and demos that simplify and
accelerate implementation of
applications like robotics ...

Lattice Semiconductor Corporation:
Lattice Automate Solution Stack
Accelerates Development of Industrial
Automation Systems
Analyst Hans Mosesmann with
Rosenblatt Securities has a \$135 price
target on the stock, representing more
than 40% upside to AMD's price of
\$94 at the time of writing. While
Mosesmann's price ...

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Is AMD Stock Heading to \$135?

We have demonstrated our ability to execute on this need for high-performance programmable timing solutions across 300 applications ... We were not that specific in our guidance for Q3.

SiTime Corporation (SITM) Q2 2021 Earnings Call Transcript

These programmable artworks are dynamic - updating hourly to reflect their evolution over time (Async channel ... Provide specific products and services to you, such as portfolio management ...

This book contains the papers presented at the 14th International

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Conference on Field Programmable Logic and Applications (FPL) held during August 30th - September 1st 2004. The conference was hosted by the Interuniversity Micro- Electronics Center (IMEC) in Leuven, Belgium. The FPL series of conferences was founded in 1991 at Oxford University (UK), and has been held annually since: in Oxford (3 times), Vienna, Prague, Darmstadt, London, Tallinn, Glasgow, Villach, Belfast, Montpellier and Lisbon. It is the largest and oldest conference in reconfigurable computing and brings together academic researchers, industry experts, users and newcomers in an informal, welcoming atmosphere that encourages productive exchange of ideas and knowledge between the delegates. The fast and exciting advances in field programmable logic are increasing

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steadily with more and more application potential and need. New ground has been broken in architectures, design techniques, (partial) run-time reconfiguration and applications of field programmable devices in several different areas. Many of these recent innovations are reported in this volume. The size of the FPL conferences has grown significantly over the years. FPL in 2003 saw 216 papers submitted. The interest and support for FPL in the programmable logic community continued this year with 285 scientific papers submitted, demonstrating a 32% increase when compared to the year before. The technical program was assembled from 78 selected regular papers, 45 additional short papers and 29 posters, resulting in this volume of proceedings. The program

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also included three invited plenary keynote presentations from Xilinx, Gilmer Technology Report and Altera, and three embedded tutorials from Xilinx, the Universit at Karlsruhe (TH) and the University of Oslo.

This is the first book on the topic of all source positioning, navigation and timing (PNT) and how to solve the problem of PNT when the most widely-used measurement source available today, the GPS system, may be come unavailable, jammed or spoofed. Readers learn how to define the system architecture as well as the algorithms for GPS-denied and GPS-challenged PNT systems. In addition, the book provides comprehensive coverage of the individual technologies

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used, such as celestial navigation, vision-based navigation, terrain referenced navigation, gravity anomaly referenced navigation, signal of opportunity (SOO) based PNT, and collaborative PNT. Celestial Navigation is discussed, with stars and satellite used as reference, and star-tracker technology also included. Propagation based timing solutions are explored and the basic principles of oscillators and clocks presented. Initial alignment of strap-down navigation systems is explored, including initial alignment as a Kalman filter problem. Velocimeter/Dead reckoning based navigation and its impact on visual odometry is also explained. Covering both theoretical and practical issues, and packed with equations and models, this book is useful for both the engineering student

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as well as the advanced practitioner.

Solutions

Embedded systems are characterized by the presence of processors running application-specific software. Recent years have seen a large growth of such systems, and this trend is projected to continue with the growth of systems on a chip. Many of these systems have strict performance and cost requirements. To design these systems, sophisticated timing analysis tools are needed to accurately determine the extreme case (best case and worst case) performance of the software components. Existing techniques for this analysis have one or more of the following limitations: they cannot model complicated programs they cannot model advanced micro-architectural features of the processor, such as cache

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memories and pipelines they cannot be easily retargeted for new hardware platforms. In Performance Analysis of Real-Time Embedded Software, a new timing analysis technique is presented to overcome the above limitations. The technique determines the bounds on the extreme case (best case and worst case) execution time of a program when running on a given hardware system. It partitions the problem into two sub-problems: program path analysis and microarchitecture modeling. Performance Analysis of Real-Time Embedded Software will be of interest to Design Automation professionals as well as designers of circuits and systems.

This book constitutes the thoroughly refereed post-conference proceedings of 18th International Workshop on

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Power and Timing Modeling, Optimization and Simulation, PATMOS 2008, featuring Integrated Circuit and System Design, held in Lisbon, Portugal during September 10-12, 2008. The 31 revised full papers and 10 revised poster papers presented together with 3 invited talks and 4 papers from a special session on reconfigurable architectures were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on low-leakage and subthreshold circuits, low-power methods and models, arithmetic and memories, variability and statistical timing, synchronization and interconnect, power supplies and switching noise, low-power circuits; reconfigurable architectures, circuits and methods, power and delay modeling, as well as power

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This book includes a range of techniques for developing digital signal processing code; tips and tricks for optimizing DSP software; and various options available for constructing DSP systems from numerous software components.

The consumer electronics market has never been as awash with new consumer products as it has over the last couple of years. The devices that have emerged on the scene have led to major changes in the way consumers listen to music, access the Internet, communicate, watch videos, play games, take photos, operate their

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automobiles even live. Digital electronics has led to these leaps in product development, enabling easier exchange of media, cheaper and more reliable products, and convenient services. This handbook is a much-needed, comprehensive engineering guide to the dynamic world of today's digital consumer electronics. It provides complete details on key enabling technologies, standards, delivery and reception systems, products, appliances and networking systems. Each chapter follows a logical progression from a general overview of each device, to market dynamics, to the core technologies and components that make up that particular product. The book thoroughly covers all of the key digital consumer product categories: digital TV, digital audio, mobile

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communications devices, gaming consoles, DVD players, PCs and peripherals, display devices, digital imaging devices, web terminals and pads, PDAs and other handhelds, screenphones/videophones, telematics devices, eBooks and readers, and many other current and future products. To receive a FREE daily newsletter on displays and consumer electronics, go to: <http://www.displaydaily.com/> ·Surveys crucial engineering information for every digital consumer product category, including cell phones, digital TVs, digital cameras, PDAs and many more—the only reference available to do so ·Has extremely broad market appeal to embedded systems professionals, including engineers, programmers, engineering managers, marketing and sales

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Personnel 1,000,000+ potential readers ·Helps engineers and managers make the correct design decisions based on real-world data

This book contains the articles from the international conference 11th Workshop on Self-Organizing Maps 2016 (WSOM 2016), held at Rice University in Houston, Texas, 6-8 January 2016. WSOM is a biennial international conference series starting with WSOM'97 in Helsinki, Finland, under the guidance and direction of Professor Tuevo Kohonen (Emeritus Professor, Academy of Finland). WSOM brings together the state-of-the-art theory and applications in Competitive Learning Neural Networks: SOMs, LVQs and related paradigms of unsupervised and supervised vector quantization. The

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Current proceedings present the expert body of knowledge of 93 authors from 15 countries in 31 peer reviewed contributions. It includes papers and abstracts from the WSOM 2016 invited speakers representing leading researchers in the theory and real-world applications of Self-Organizing Maps and Learning Vector Quantization: Professor Marie Cottrell (Universite Paris 1 Pantheon Sorbonne, France), Professor Pablo Estevez (University of Chile and Millennium Institute of Astrophysics, Chile), and Professor Risto Miikkulainen (University of Texas at Austin, USA). The book comprises a diverse set of theoretical works on Self-Organizing Maps, Neural Gas, Learning Vector Quantization and related topics, and an excellent variety of applications to data visualization,

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clustering, classification, language processing, robotic control, planning, and to the analysis of astronomical data, brain images, clinical data, time series, and agricultural data.

This book constitutes the refereed proceedings of the 10th International Workshop on Power and Timing Modeling, Optimization and Simulation, PATMOS 2000, held in G ttingen, Germany in September 2000. The 33 revised full papers presented were carefully reviewed and selected for inclusion in the book. The papers are organized in sections on RTL power modeling, power estimation and optimization, system-level design, transistor level design, asynchronous circuit design, power efficient technologies, design of multimedia processing applications,

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adiabatic design and arithmetic
modules, and analog-digital circuit
modeling.

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